

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	320	703/25.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/03 14:28
L2	251	703/28.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/03 14:28
S2	2	"20040172233"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/03 14:28
S5	2	"20050125214"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 10:33
S6	0	ICE and SOC near6 emulat\$3 same reset vector	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 10:33
S7	6	(ICE) and (system near3 chip or microcontroller) same emulat\$3 and reset same vector	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 10:34
S8	38	(in\$circuit adj emulat\$3) and (system near3 chip or microcontroller) same emulat\$3 and reset same vector	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:14
S17	47	(circuit adj emulat\$3) and (system near3 chip or (micro-controller or microcontroller)) same emulat\$3 and reset same vector	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:19

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S18	160	(circuit adj emulat\$3) and (system near3 chip or (microcontroller or micro-controller)) same emulat\$3 and reset and vector	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:19
S19	23	(circuit adj emulat\$3) and (system near3 chip or micro-controller) same emulat\$3 and reset same vector	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:22
S26	44	(S17 or S18 or S19) ICE	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:23
S29	27	(S17 or S18 or S19) reset near3 vector	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:38
S30	47	(circuit adj emulat\$3) and (system near3 chip or (micro-controller or microcontroller)) same emulat\$3 and reset same vector	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:26
S31	160	(circuit adj emulat\$3) and (system near3 chip or (microcontroller or micro-controller)) same emulat\$3 and reset and vector	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:26
S32	23	(circuit adj emulat\$3) and (system near3 chip or micro-controller) same emulat\$3 and reset same vector	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:26
S33	44	(S30 or S31 or S32) ICE	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:26
S34	4	S33 and reset near3 vector	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:26

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S35	2	S33 and reset near3 address	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:34
S36	2	"20050125214"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:36
S37	251	703/28.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:36
S38	19	S37 (S30 or S31 or S32)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:37
S39	0	S38 (S34 or S35)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:37
S40	27	(S30 or S31 or S32) reset near3 vector	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:54
S41	4	S40 ICE	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:39
S42	2	"5357626".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:40
S43	2	"5566303".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:54

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S44	1	(S43 or S42) ICE	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:40
S45	20	S40 (break request)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:55
S46	2	S45 "reset signal"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 12:59
S47	1	ICE.ab. ("reset signal" and "break request")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 13:00
S48	0	S45 "low power"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 13:00
S49	1	ICE "reset signal" and "break request" "low power consumption"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 13:00
S50	10	ICE "reset signal" and "break request" "power consumption"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 14:01
S51	0	713/300.ccls. 713/1.ccls. 703/28.ccls. 714/28-30.ccls. 710/4.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 16:13
S52	60	703/28.ccls. 714/28-30.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 14:02

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S53	5	S52 (ICE "reset signal" "break request")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 14:17
S54	0	713/300.ccls. 713/1.ccls. 710/4.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 14:18
S55	2978	713/300.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 14:18
S56	2622	713/1.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 14:18
S57	252	710/4.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 14:18
S58	0	(S55 or S56 or S57) ICE "rest signal" "break request"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 14:18
S59	2622	713/1.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 16:11
S60	1268	714/28-30.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 16:11
S61	8	(S59 or S60) ICE "reset signal" "break request"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/07/31 16:14

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S62	251	703/28.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/08/03 13:56
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